

National Conference
on
“VLSI, Signal Processing & Communications”
Organized by
Department of ECE
In Association with
IETE Chapter, Vijaywada

Conference Schedule

Day1 (Fri, 10-02-2012):

08:00AM onwards	--	Registration
09.00AM – 10.30AM	--	Inauguration by Dr. RV Raja Kumar
10.30 AM -11.30 AM	--	Tea Break
11.30AM – 12.00PM	--	Key note address by Dr. B. Prahlada Rao, C-DAC, Bangalore
12.00PM – 01.00PM	--	Session – 1 (Embedded Systems & Digital Design)
02.00PM – 03.30PM	--	Session-2 (Image Processing)
04.00PM – 04.30PM	--	Key note by Dr.S. Srinivasa Kumar, JNTU, Kakinada
04.30PM – 05.30PM	--	Session-3 (Image Processing)

Day2 (Sat, 11-02-12):

9.00AM – 10.30AM	--	Session-4 (Communications & Digital Signal Processing)
10.30 AM -11.00 AM	--	Tea Break
11.00AM – 11.30AM	--	Key note address by Dr.T.Kishore Kumar, NIT, Warangal
11.30AM – 01.00PM	--	Session – 5 (Communications)
02.00PM – 03.30PM	--	Session – 6 (VLSI, MEMs, Fuzzy Logic & Nano Technology)
03.30PM – 04.00PM	--	Tea Break
04.00PM – 05.00PM	--	Valedictory Function
